450100-03853

## THE UNITED STATES PATENT AND TRADEMARK OFFICE

Applicant

Masaki ENDO

Serial No.

10/084,789

Filed

February 25, 2002

For

DELAY LOCK LOOP CIRCUIT, VARIABLE DELAY CIRCUIT,

AND RECORDING SIGNAL COMPENSATING CIRCUIT

Art Unit

2816

745 Fifth Avenue New York, New York 10151 Tel. (212) 588-0800

I hereby certify that this correspondence is being deposited with the United States Postal Service as first class mail in an envelope addressed to: Assistant Commissioner for Patents Washington, D.C. 20231, on April 19, 2002

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Gordon Kessler, Reg. No. 38,511 Name of Applicant, Assignee or gistered Representative

Signatur

April 🔑

Date of Signature

## INFORMATION DISCLOSURE STATEMENT <u>UNDER 37 C.F.R. 1.97(b)</u>

Assistant Commissioner for Patents Washington, D.C. 20231

Sir:

In compliance with the duty of disclosure set forth in 37 C.F.R. 1.56, applicant is filing this Information Disclosure Statement and the accompanying form PTO-1449 along with copies of the prior art therein identified. So far as applicant is aware,

this Information Disclosure Statement is being filed before the mailing date of the first Office Action on the merits.

The filing of this Information Disclosure Statement is not an admission that the documents identified herein constitute prior art to the present application.

Please charge any fees incurred by the filing of this document or credit any overpayment to Deposit Account No. 50-0320.

Respectfully submitted,

FROMMER LAWRENCE & HAUG LLP Attorneys for Applicant

Rv.

Gordon Kessler

Reg. No. 38,511

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Based on Form PIO 149  45  APR 2  LIST OF REFERENCES CITED BY APPLICANT  Ma  Several sheets if necessary)  FI						ATTY. DOCKET NO. 450100-03853		SERIAL NO. 10/084,789		
						APPLICANT Masaki ENDO				
						FILING DATE February 25, 2002		GROUP 2816		
U.S. PATENT DOCUMENTS										
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	АМ	JP 2000 134072		05/12/00	Japan				Abstract	
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OTHER PRIOR ART (Including Author, Title, Date, Pertinent Pages, Etc.)										
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